

InfoTracks

Semitracks Monthly Newsletter

New Semitracks Blog!

In order to keep our readers better up-to-date and informed, Semitracks Inc. has launched its new blog!

Read more, Page 3



Alternate Channel Materials for High Mobility CMOS

By Christopher Henderson

This year's International Electron Device Meeting (IEDM) discussed a wide range of approaches for creating CMOS transistors with better performance. One axis of performance that is important is higher speed for faster switching. Higher speed invariably means higher mobility, so researchers have been investigating techniques to make this happen.

Silicon has a moderate mobility, but there are methods to improve it marginally. This can be done using strain. Companies like Intel and TSMC offer technologies and devices that use strained silicon. To get a larger improvement, one needs to consider alternate materials.

This table shows the major materials and candidates that can be or are used in CMOS devices. Silicon is the existing material for most devices. The electron and hole mobility for silicon are reasonable, but they're not the highest numbers that are potentially available. Germanium has a much higher electron mobility and a lower electron mass. More importantly, germanium has the highest hole mobility of any of the major semiconductor materials at $1900 \text{ cm}^2/\text{V}\cdot\text{sec}$. It also has the lowest mass for heavy holes, which is the main type of carrier in a p-channel transistor. Some companies, like IBM, mix Si and Ge together in the transistor channel to improve mobility. GaAs, InP, InAs, and InSb all have even higher mobilities. InAs and InSb have the highest mobilities, but the bandgaps for these materials are quite low. GaAs and InP have a larger bandgap, which makes them more useful in low power applications.

Continued on Page 2

In this Issue:

Pages 1 & 2	Alternate Channel Materials for High Mobility CMOS Part I
Page 2	PV Manufacturing and Troubleshooting Course
Page 3	Ask the Experts: Small Die Decapsulation
Page 3	Semitracks Blog
Page 3&4	Technical Tidbit: Energy/Band Structure Diagram
Page 4	Upcoming Courses



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Semiconductor, Microelectronics, Microsystems, and Nanotechnology Training

(continued)

Material	Si	Ge	GaAs	InP	InAs	InSb
Electron Mobility (cm ² /V-sec)	1600	3900	9200	5400	40000	77000
Eff. Electron Mass (/m _o)	m _t : 0.19 m _i : 0.916	m _t : 0.082 m _i : 1.467	0.067	0.08	0.026	0.0135
Hole Mobility (cm ² /V-sec)	430	1900	400	200	500	850
Eff. Electron Mass (/m _o)	m _{HH} : 0.49 m _{LH} : 0.16	m _{HH} : 0.28 m _{LH} : 0.044	m _{HH} : 0.45 m _{LH} : 0.082	m _{HH} : 0.45 m _{LH} : 0.12	m _{HH} : 0.57 m _{LH} : 0.35	m _{HH} : 0.44 m _{LH} : 0.016
Bandgap (eV)	1.12	0.66	1.42	1.34	0.36	0.14
Permittivity	11.8	16	12	12.6	14.8	17

Stay tuned for the conclusion of this article in January!



PV Manufacturing and Troubleshooting Course
Toronto, Canada – January 18, 2012
 For more information, click [here](#)
 To register, click [here](#)
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Ask the Experts

Q: How do I keep from losing a very small die when I decapsulate it with sulfuric acid?

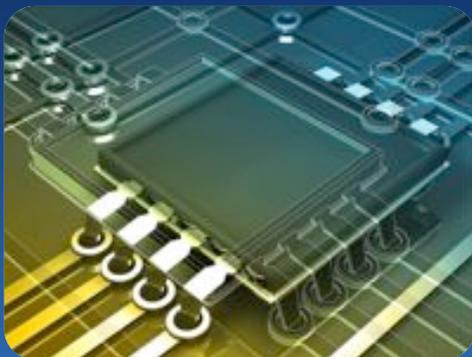
A: Probably the best way to keep from losing a very small die is to put the package in a fine wire mesh bag and dip the bag in the sulfuric acid. That way, the die and lead frame stay contained. You can then dip the bag in isopropyl alcohol to stop the etch, rinse the bag in deionized water, and retrieve the die and lead frame under a low power microscope.

*To post, read, or answer a question, [visit our forums](#).
We look forward to hearing from you!*

Technical Tidbit: How to Interpret an Energy/Band Structure Diagram

Energy/Band Structure Diagrams seem arcane, something that one learns for their college device physics course and then forgets. However, there is a lot of useful information in the diagram. Band structure diagrams measure energy as a function of wave vector. The wave vector is best understood by looking at the crystal structure. This figure shows a portion of the crystal structure for diamond and zinblende lattice structures. The center point of the crystal is given by gamma (Γ) and

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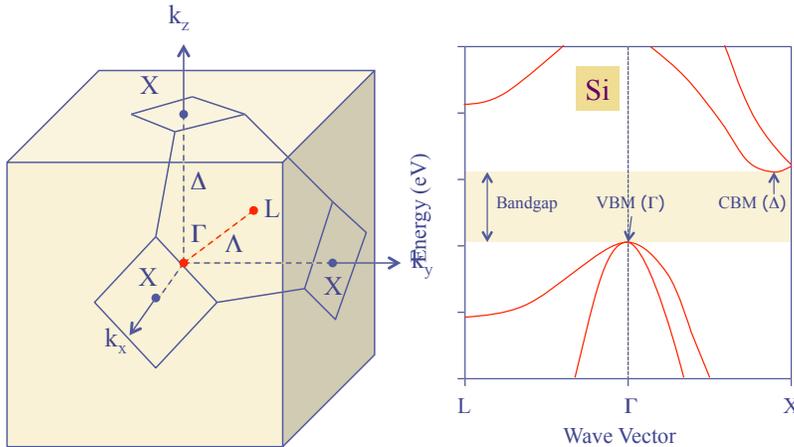


Semitracks has started a blog to keep you up to date on industry developments and items that affect Semiconductor Product Engineering and Reliability. In addition to industry developments, we'll include some short articles on technology items of interest. These may vary from historical items that help place current developments in context, to future developments that are likely to affect the industry. If you have comments or feedback, or topics you would like to see addressed, please feel free to e-mail us at info@semitracks.com.

See it for yourself at:

<http://www.semitracks.com/index.php/en/blog>

the faces of the crystal are given by X and L respectively. The X faces represent the intersections of the crystal lattice with the box (the [100] axis), and the L faces represent the intersections of the remaining faces of the crystal (the [111] axis). This structure is known as the Brillouin zone.



The figure on the right shows the energy band structure for silicon. The red lines show the energy lines as a function of k -space or wave vector. The red lines above the bandgap denote the conduction bands, and the lines below indicate the valence bands. The distance between the lowest energy in the conduction band and the maximum energy in the valence band represents the band gap energy. For silicon, it is 1.11eV. Notice that the minimum in the conduction band does not line up with the maximum in the valence band. This is because silicon is an indirect band gap material. If the minimum and maximum line up in k -space, the material is a direct band gap material. The shape of the bands relates to the mobility of the semiconductor. If the valence band has a sharp bend near the maximum point, then the mobility will be high for holes. If the conduction band has a sharp bend near minimum point, the electron mobility will be high. The outer valence band indicates the energy structure for heavy holes, while the inner valence band indicates the energy structure for light holes. Some energy diagrams show structure at higher energies (+3eV and higher) and lower energies (-3eV and lower). This structure is not as important for semiconductor operation as very few electrons and holes operate in these regions.

To post, read, or answer a question, [visit our forums](#).
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Upcoming Courses

[Semiconductor Reliability](#)

January 17-19, 2012 – San Jose

[Wafer Fab Processing](#)

January 17-20, 2012 – San Jose

[Polymers in Electronics/FTIR](#)

January 17-18, 2012 - Phoenix

[PV Manufacturing](#)

January 18, 2012 – Toronto, Canada

[Failure and Yield Analysis](#)

January 23-26, 2012 – Cambridge, UK

Feedback

If you have a suggestion or a comment regarding our courses, online training, discussion forums, or reference materials, or if you wish to suggest a new course or location, please call us at 1-505-858-0454 or e-mail us at info@semitracks.com.

To submit questions to the Q&A section, inquire about an article, or suggest a topic you would like to see covered in the next newsletter, please contact Jeremy Henderson by email at jeremy.henderson@semitracks.com.

We are always looking for ways to enhance our courses and educational materials.

For more information on Semitracks online training or public courses, visit our website!

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